

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Leonard J. Olmer, et al.

Serial No.: 10/675,575

Filed: September 30, 2003

Title: IN-SITU REMOVAL OF SURFACE IMPURITIES PRIOR TO ARSENIC-
DOPED POLYSILICON DEPOSITION IN THE FABRICATION OF A
HETEROJUNCTION BIPOLAR TRANSISTOR

Grp./A.U.: 1746

Examiner: Zeinab El Arini

Confirmation No. 9967

For:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed
with United States Patent and Trademark Office on July 6, 2007

Mary Balho
(Printed or typed name of person signing the certificate)

(Mary Balho)
(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed April 10, 2007, please amend the above-
identified application as follows: